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### Tradeoffs and Optimization in Analog CMOS Design

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### Tradeoffs and Optimization in Analog CMOS Design

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Tradeoffs and optimization in analog CMOS design. Binkley, David M. John Wiley & Sons 2008 594 pages \$130.00 Hardcover TK7871 What are difficulties are in fact opportunities, an axiom certainly proved in the case of the design of analog complementary metal-oxide semiconductor (CMOS) integrated circuits.

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